



**RESPONSE UNDER 37 C.F.R. § 1.116
EXPEDITED PROCEDURE
GROUP ART UNIT 2181**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application. No: 10/602,292

Filed: June 24, 2003

Inventor(s):

Michael B. Doerr, William H.

Hallidy, David A. Gibson and

Craig M. Chase

Title: PROCESSING SYSTEM
WITH INTERSPERSED
PROCESSORS AND
COMMUNICATION
ELEMENTS

Examiner: Lai, Vincent

Group/Art Unit: 2181

Atty. Dkt. No: 5860-00101

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, Alexandria, VA 22313-1450, on the date indicated below.

Mark S. Williams

July 31, 2006
Date

Mark S. Wilkins
Signature

**RESPONSE TO OFFICE ACTION OF
JUNE 2, 2006**

Mail Stop AF

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of June 2, 2006, to further highlight why the application is in condition for allowance.